60. (New) A method for accessing a storage device, comprising:

receiving a first address to the storage device;

receiving a burst/pipeline signal;

selecting between outputting information from the storage device and inputting information to the storage device;

selecting between an asynchronously-accessible burst mode and an asynchronously-accessible pipelined mode of operation of the storage device in response to the burst/pipeline signal;

obtaining a second address to the storage device; and

asynchronously accessing a storage element of the storage device in the selected mode of operation using the first address and the second address.

61. (New) A method for accessing several different locations in an asynchronously-accessible memory device, comprising:

selecting a pipeline mode of operation;

providing an initial external address associated with asynchronously accessing the asynchronously-accessible memory device in the pipelined mode of operation;

generating at least one subsequent internal address patterned after the initial external address while in the pipelined mode of operation;

switching modes to a burst mode of operation; and

providing a new external address for every access associated with asynchronously accessing the asynchronously-accessible memory device while in the burst mode of operation.

62. (New) A method of specifying burst or pipeline access to a memory, comprising:

receiving an external row address;

receiving a pipeline/burst select signal;

selecting an external address path if the pipeline/burst signal indicates a pipeline mode of operation; and